

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: MORIWAKI, et al.
Serial No.: Not Yet Assigned
Filed: On Even Date
For: PACKET COMMUNICATION SYSTEM

J1011 U.S. Prg
10/04/6304
01/15/02

#3

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97 & 1.98

Assistant Commissioner for Patents
Washington, D.C. 20231

January 16, 2002

Sir:

In the matter of the above-identified application, applicants are submitting herewith a copy of a communication from a foreign patent office in a counterpart foreign application and copies of the documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted with the new application.

Each of the documents listed on the attached form equivalent to Form PTO-1449 is in the English language.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (Case: 501.41034X00), and please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS LLP

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Attachments

Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DKT. NO. 501.41034X00	SERIAL NO. J1011046304
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)			APPLICANT MORIWAKI, et al.	
			FILING DATE January 16, 2002	GROUP

J1011046304
U.S.PTO
01/16/02

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						
AK						
AL						

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
AM							
AN							
AO							
AP							
AQ							
AR							
AS							
AT							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AU	McKEOWN, et al., "Tiny Tera: A Packet Switch Core", IEEE Micro, January/February 1997, pps. 26-33
AV	
AW	
AX	
AY	
AZ	

Examiner	Date Considered
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